

## 36 GHz dual-modulus prescaler in SiGe bipolar technology

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*H. Knapp, M. Wurzer, J. Bock, T.F. Meister, G. Ritzberger and K. Aufinger. "36 GHz dual-modulus prescaler in SiGe bipolar technology." 2002 Radio Frequency Integrated Circuits (RFIC) Symposium 02. (2002 [RFIC]): 239-242.*

Presents a dual-modulus prescaler with divide ratios of 256 and 257. The circuit uses static divider stages and differential current-mode logic. AND-gates are merged with flip-flops to achieve high operating frequencies at low power consumption. The prescaler operates with input frequencies ranging from below 1 GHz up to 36.4 GHz. It consumes 34.2 mA from a 3 V supply. The circuit is manufactured in a 0.4  $\mu\text{m}$  SiGe bipolar technology.

 [Return to main document.](#)